

Berry IO Board Schematics Document

Arrandale

Intel PCH

2010-02-04

REV : A00

DY : Nopop Component

WWAN : Pop for WWAN

TP : Pop for Touch Panel

<Core Design>



Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Cover Page

Size
A3

Document Number

Berry IO Board

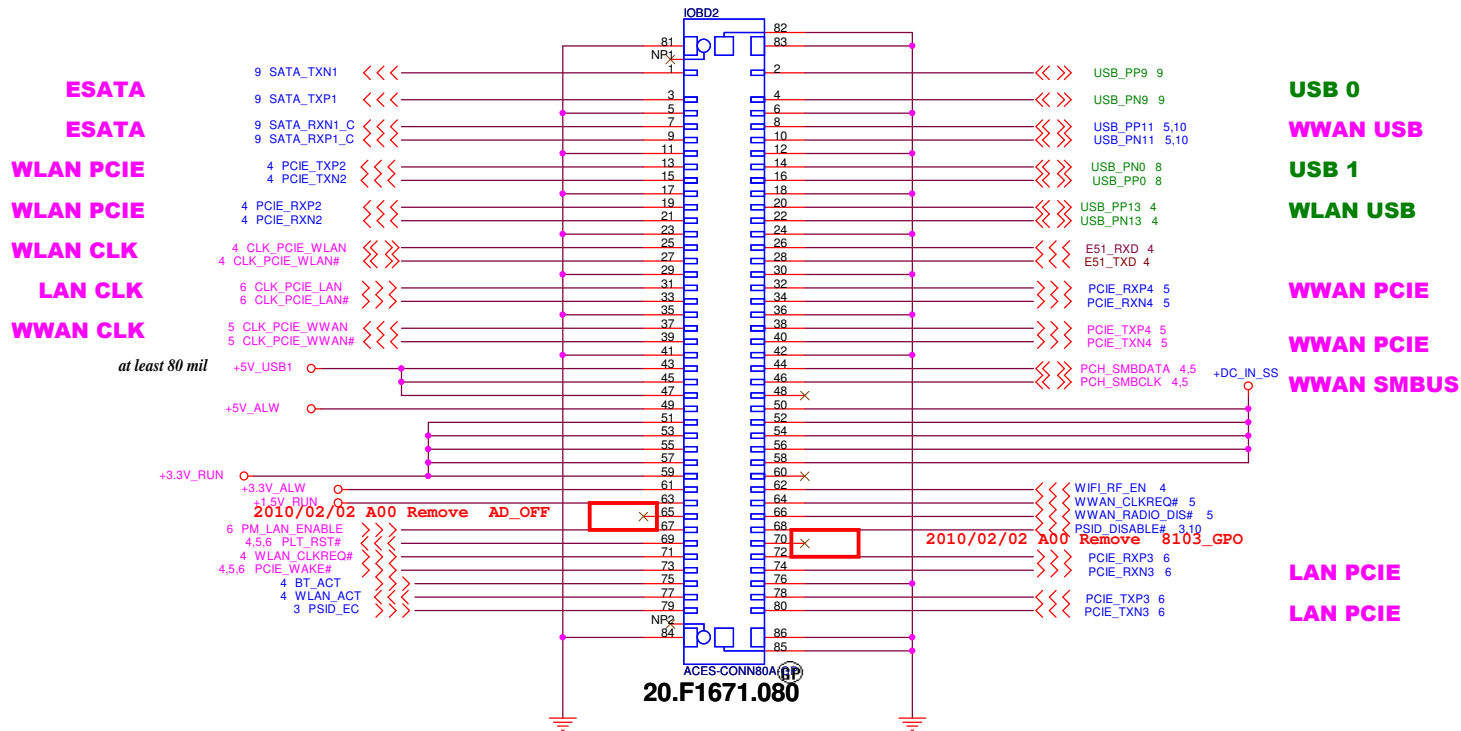
Rev

A00

Date: Thursday, February 04, 2010

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Project code:91.4HH01.001
PCB P/N :09697-1
REVISION :-1

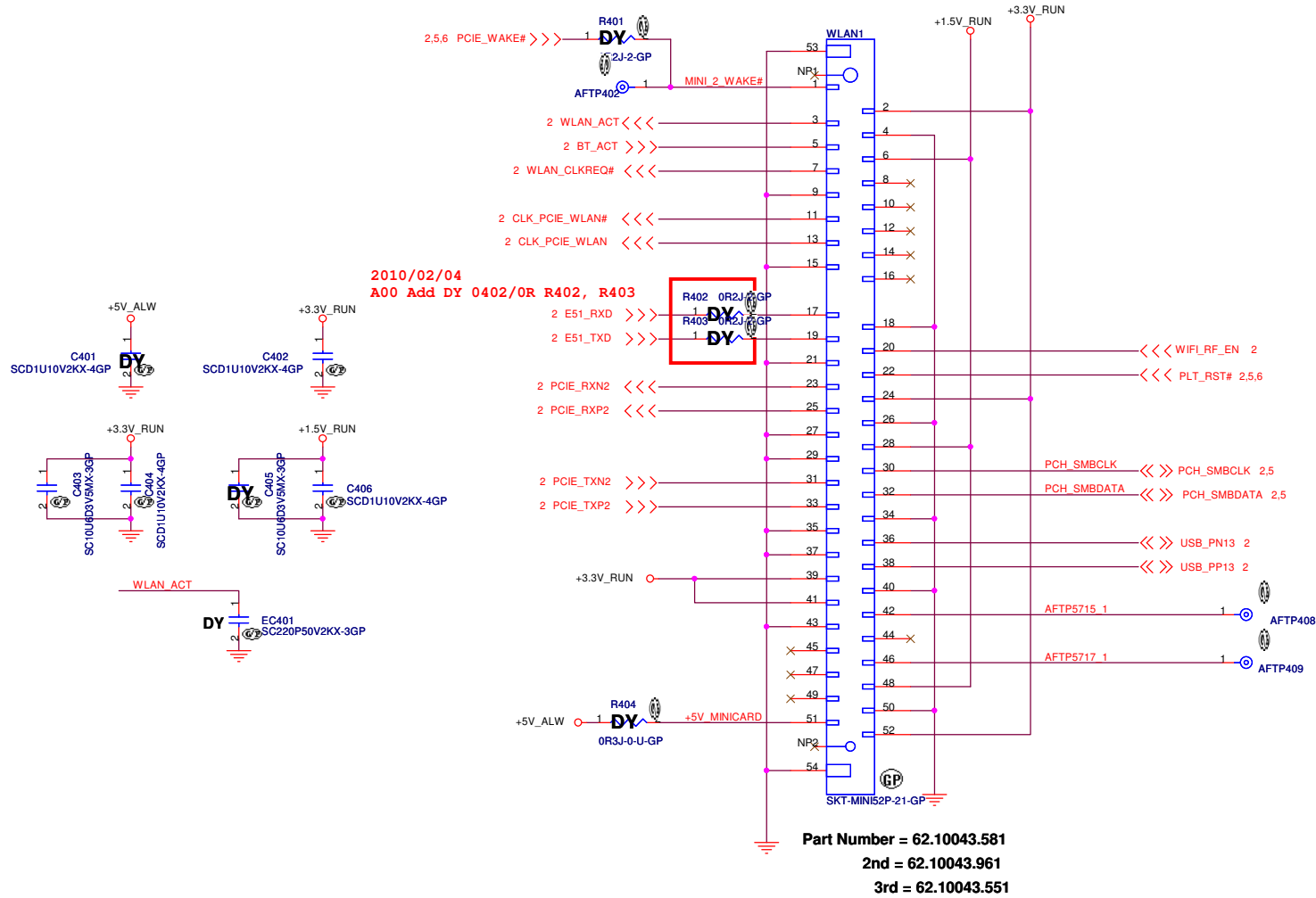


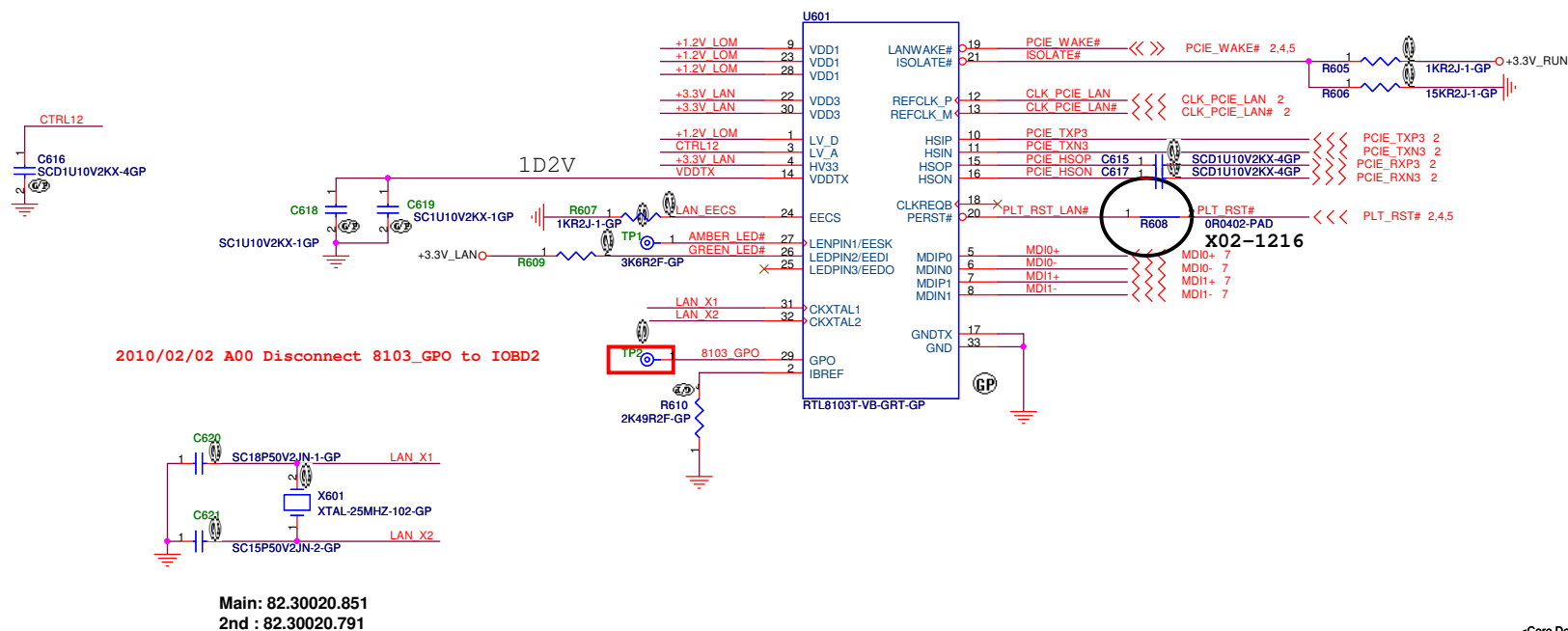
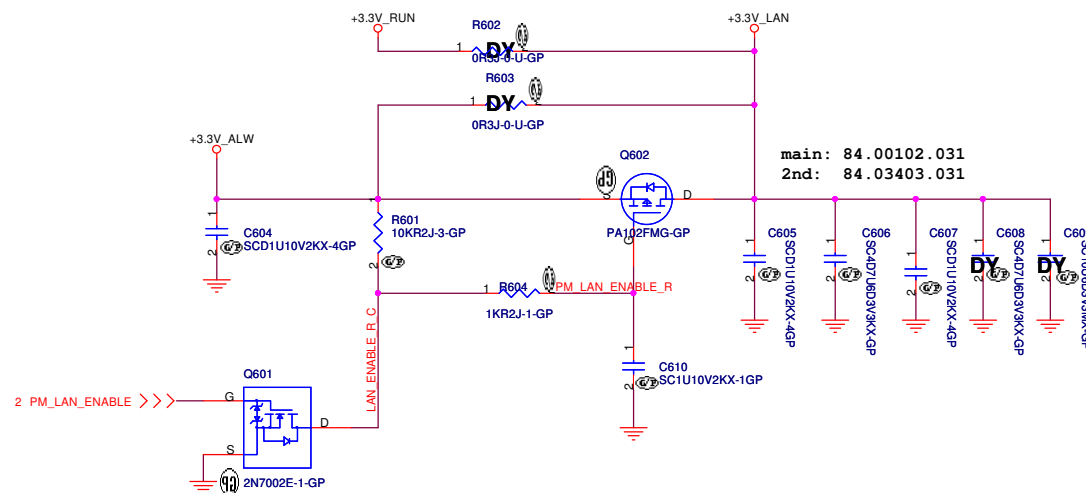
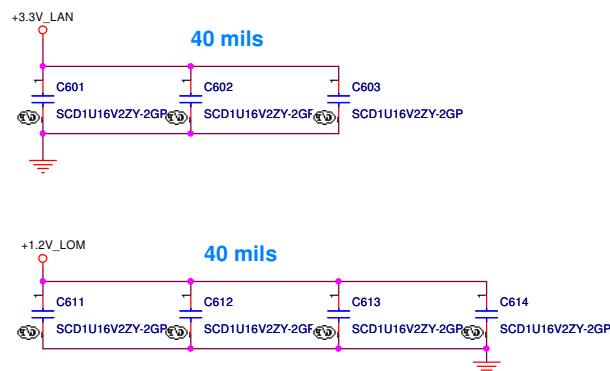
PCIE_RX Decoupling Capacitor near LAN Chip



SSID = Wireless

Mini Card Connector(802.11a/b/g)



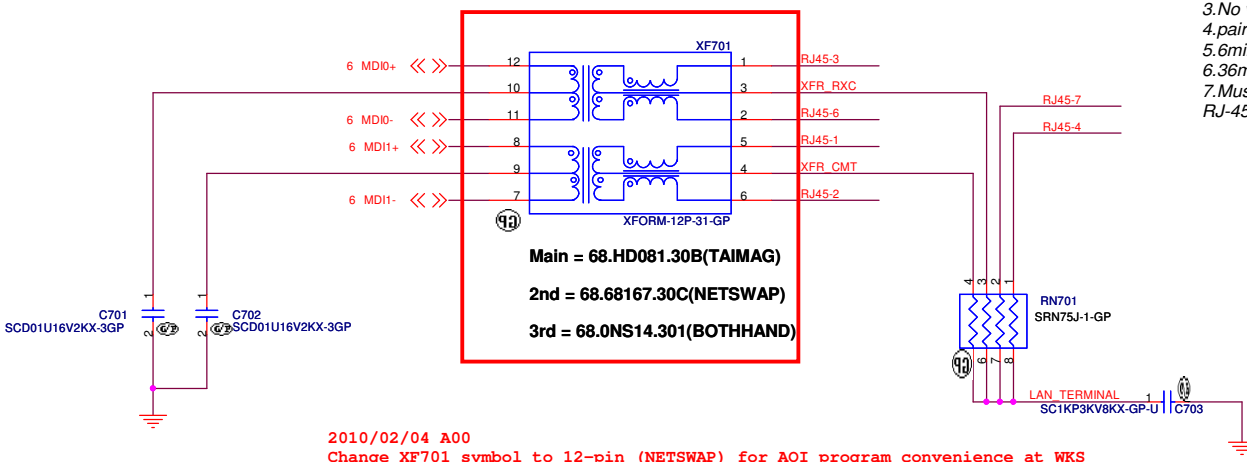


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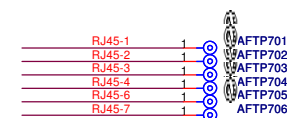
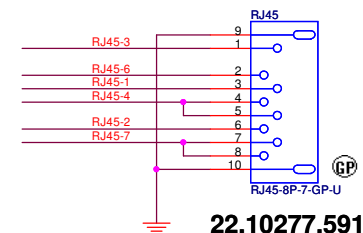
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10/100M Lan Transformer



- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat, except RJ-45 moat.

RJ45 Connector

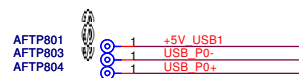


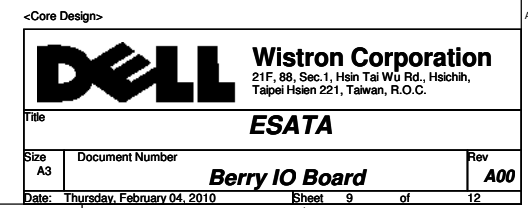
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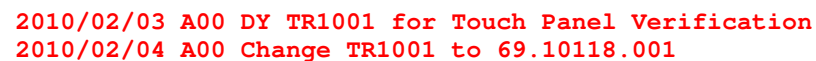


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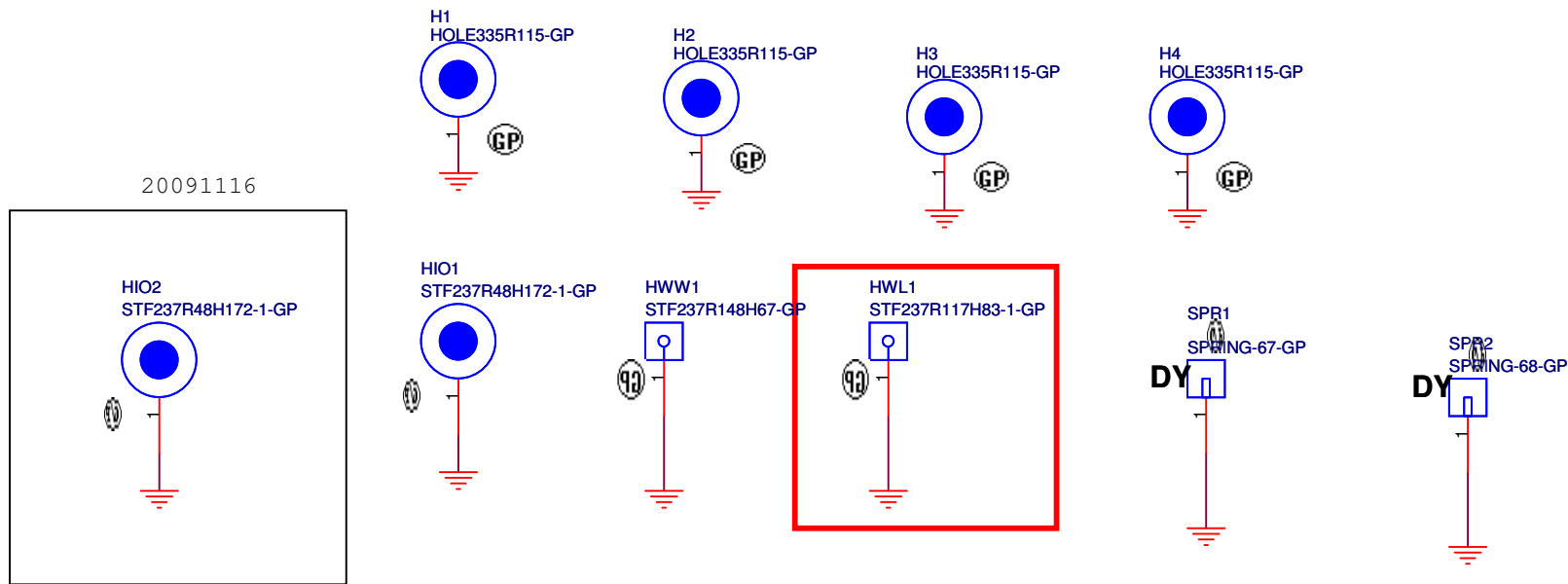
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Change List

Size

A3

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Date: Wednesday, January 20, 2010

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